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IPR 3628

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Total Number of Pages in This Submission

Application Number	10/066,095
Filing Date	1/31/2002
First Named Inventor	Steven Teig, et al.
Group Art Unit	3628
Examiner Name	Chencinski, S.
Total Number of Pages in This Submission	Attorney Docket Number
	SPLX.P0074

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Firm Or Individual name	Mani Adeli, Reg. No. 39,585 of Stattler Johansen & Adeli LLP
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Date	May 14, 2004

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PATENT  
Docket No. SPLX.P0074

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Mani Adel

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig, et al.

Serial No.: 10/066,095

Filing Date: 1/31/2002

For: METHOD AND APPARATUS FOR  
IDENTIFYING A SET OF PATHS

**INFORMATION DISCLOSURE  
STATEMENT UNDER 37 C.F.R. § 1.97**

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This Information Disclosure Statement under 37 C.F.R. § 1.97 is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to

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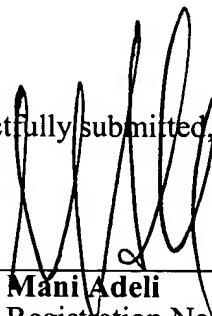
This Information Disclosure Statement is submitted within three months of the application filing date or before the mailing date of a first Office Action on the merits; accordingly, no fee or separate requirements are required.

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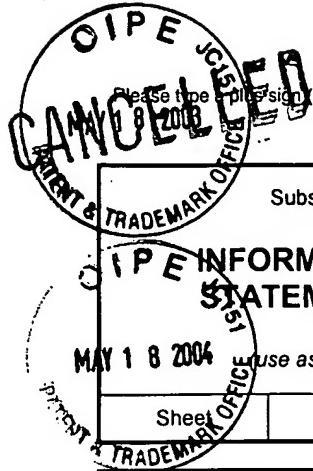
Respectfully submitted,

By:

  
Mani Adeli  
Registration No. 39,585

Stattler, Johansen & Adeli LLP  
P.O. Box 51860  
Palo Alto, CA 94303-0728

Phone No. (650) 752-0990 x102  
Fax No. (650) 752-0995



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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet

1

of

9

Application Number

10/066,095

Filing Date

1/31/2002

First Named Inventor

Steven Teig et al.

Group Art Unit

3628

Examiner Name

Chencinski, S.

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Sheet

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Attorney Docket Number

SPLX.P0074

### U.S. PATENT APPLICATIONS

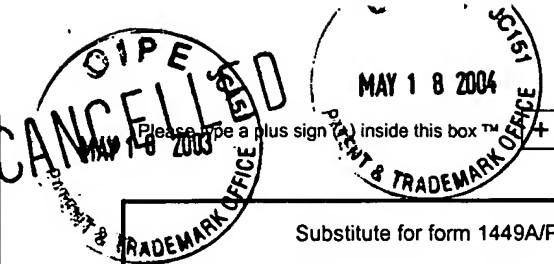
Examiner* Initials	Cite No. <sup>1</sup>	U.S. Patent Application		Name of Patentee or Applicant of Cited Document	Date of Filing MM-DD-YYYY	Related Application Data if any
		Serial Number	Attorney Docket Number			
	1.	10/066,060	SPLX.P0072	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	2.	10/066,160	SPLX.P0073	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	3.	10/066,047	SPLX.P0078	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	4.	10/061,641	SPLX.P0079	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	5.	10/066,094	SPLX.P0080	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	6.	10/076,121	SPLX.P0081	Steven Teig et al.	02-12-2002	CIP of 10/066,094.
	7.	10/062,995	SPLX.P0105	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	8.	10/066,102	SPLX.P0106	Steven Teig	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	9.	10/066,187	SPLX.P0133	Steven Teig et al.	01-31-2002	Application filed on the same date, with same specification and drawings, but with different summary and abstract.
	10.	10/286,584	CDN.P0037	Steven Teig	10-31-2002	
	11.	10/335,087	CDN.P0038	Steven Teig et al.	12-31-2002	
	12.	10/335,239	CDN.P0039	Steven Teig et al.	12-31-2002	

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<p>Substitute for form 1449A/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p>(use as many sheets as necessary)</p>				Application Number	10/066,095
				Filing Date	1/31/2002
				First Named Inventor	Steven Teig et al.
				Group Art Unit	3628
				Examiner Name	Chencinski, S.
Sheet	2	of	9	Attorney Docket Number	SPLX.P0074
<b>U.S. PATENT APPLICATIONS</b>					
	13.	10/335,086	CDN.P0040	Steven Teig et al.	12-31-2002

<b>FOREIGN PATENT DOCUMENTS</b>							
Examiner* Initials	Cite No. <sup>1</sup>	Foreign Patent Document		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Kind Code Number <sup>4</sup> (if known) <sup>5</sup>				
	14.	JP	11-296560	10-29-1999	Matsumoto et al.	with English translation of Abstract;	
	15.	JP	2000-082743	03-21-2000	Igarashi et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	16.	JP	64-15947	01-19-1989	Ouchi	with English translation of Abstract;	
	17.	JP	03-173471	07-26-1991	Tawada et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	18.	JP	04-000677	01-06-1992	Fujiwara et al.	with English translation of Abstract;	
	19.	JP	05-102305	04-23-1993	Sato	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
	20.	JP	05-243379	09-21-1993	Kubota	with Japanese Patent Office's English translation of Abstract;	✓

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First Named Inventor

Steven Teig et al.

Group Art Unit

3628

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Chencinski, S.

Sheet

3

of

9

Attorney Docket Number

SPLX.P0074

### FOREIGN PATENT DOCUMENTS

						and with English translation of the application.	
	21.	JP	07-086407		03-31-1995	Miura	with Japanese Patent Office's English translation of Abstract; and with English translation of the application. ✓
	22.	JP	09-162279		06-20-1997	Yoshida	with Japanese Patent Office's English translation of Abstract; and with English translation of the application. ✓

### NON PATENT LITERATURE DOCUMENTS

Examiner* Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>6</sup>
	23.	Chen, H.F. et al., A Faster Algorithm for Rubber-Band Equivalent Transformation for Planar VLSI Layouts, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 15, No. 2, February 1996, pp. 217-227.	
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				Group Art Unit	3628
				Examiner Name	Chencinski, S.
Sheet 4 of 9	4	of	9	Attorney Docket Number	SPLX.P0074

#### NON PATENT LITERATURE DOCUMENTS

28.	Fujimura, K. et al, Homotopic Shape Deformation.	
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30.	Hama, T. et al., Topological Routing Path Search Algorithm with Incremental Routability Test, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 18, No. 2, February 1999, pp. 142-150.	
31.	Kobayashi, K. et al., A New Interactive Analog Layout Methodology based on Rubber-Band Routing, UCSC-CRL-96-12, June 13, 1996.	
32.	Lim, A. et al, A Fast Algorithm To Test Planar Topological Routability, Technical Report 94-012, pp. 1-16.	
33.	Lu, Y., Dynamic Constrained Delaunay Triangulation and Application to Multichip Module Layout, A Thesis for Master of Science, UC Santa Cruz, December 1991.	
34.	Maley, F.M., Testing Homotopic Routability Under Polygonal Wiring Rules, Algorithmica 1996, 15: 1-16.	
35.	Morton, P. B. et al., An Efficient Sequential Quadratic Programming Formulation of Optimal Wire Spacing for Cross-Talk Noise Avoidance Routing, UCSC-CRL-99-05, March 10, 1999.	
36.	NN71091316, Use of Relatively Diagonal And Rectangular Wiring Planes n Multilayer Packages, September 1971, IBM Technical Disclosure Bulletin, Vol. No. 14, Issue No. 4, pp. 1316-1317.	
✓ 37.	Staepelaere, D. et al., Geometric Transformations for a Rubber-Band Sketch, A Thesis for a Master of Science in Computer Engineering, UCSC, September 1992.	
✓ 38.	Staepelaere, D. et al., Surf: A Rubber-Band Routing System for Multichip Modules, pp 18-26, 1993.	
✓ 39.	Su, J. et al., Post-Route Optimization for Improved Yield Using Rubber-Band Wiring Model, 1997 International Conference on Computer-Aided Design, pp 700-706, November 1997.	
✓ 40.	Wei-Ming Dai, W. et al., Routability of a Rubber-Band Sketch. 28 <sup>th</sup> ACM-IEEE Design Automation Conference, 1991. pp. 45-65.	
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✓ 42.	Xing, Z. et al., Shortest Path Search Using Tiles and Piecewise Linear Cost Propagation, IEEE, 2002, pp.145-158.	

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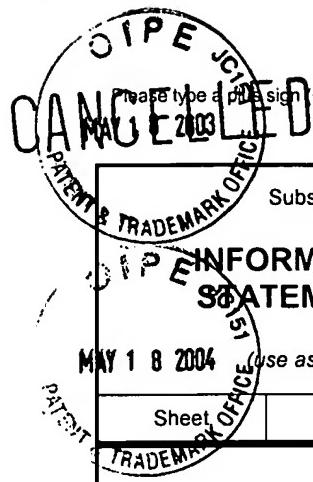
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<input checked="" type="checkbox"/>	43.	Xu, A More Efficient Distance Vector Routing Algorithm, UCSC-CRL-96-18, March 1997.	
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<input checked="" type="checkbox"/>	45.	Yu, M.-F. et al, Interchangeable Pin Routing with Application to Package Layout, UCSC-CRL-96-10, April 25, 1996.	
<input checked="" type="checkbox"/>	46.	Yu, M.-F. et al., Pin Assignment and Routing on a Single-Layer Pin Grid Array, UCSC-CRL-95-15, February 24, 1995.	
<input checked="" type="checkbox"/>	47.	Yu, M.-F. et al., Planar Interchangeable 2-Terminal Routing, UCSC-CRL-95-49, October 19, 1995.	
<input checked="" type="checkbox"/>	48.	Yu, M.-F. et al., Single-Layer Fanout Routing and Routability Analysis for Ball Grid Arrays, UCSC-CRL-95-18, April 25, 1995.	
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<input checked="" type="checkbox"/>	50.	Alexander, M. et al., Performance-Oriented Placement and Routing for field-programmable gate arrays, Proceedings of the European Design Automation Conference, pages 80-85, 1995.	
<input checked="" type="checkbox"/>	51.	Alexander, M. et al., Placement and Routing for Performance-Oriented FPGA Layout, VLSI Design, Vol. 7, No. 1, 1998.	
<input checked="" type="checkbox"/>	52.	Andou, H. et al., Automatic Routing Algorithm for VLSI, 22 <sup>nd</sup> Design Automation Conference, 1985, pp. 785-788.	
<input checked="" type="checkbox"/>	53.	Bagga, J. et al., Internal, External, and Mixed Visibility Edges of Polygons.	
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6

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9

### Application Number

10/066,095

### Filing Date

1/31/2002

### First Named Inventor

Steven Teig et al.

### Group Art Unit

3628

### Examiner Name

Chencinski, S.

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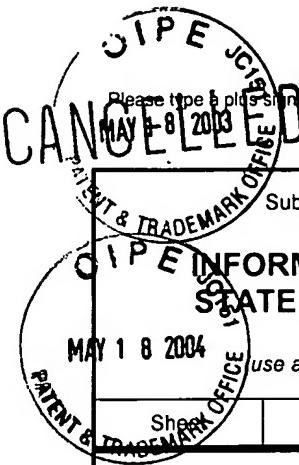
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7

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Application Number	10/066,095
Filing Date	1/31/2002
First Named Inventor	Steven Teig et al.
Group Art Unit	3628
Examiner Name	Chencinski, S.
Attorney Docket Number	SPLX.P0074

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